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## APPLICANTS

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\*\* CONTINUING DATA *None SM* \*\*\*\*\*

\*\* FOREIGN APPLICATIONS *SM* \*\*\*\*\*  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
Verified and Acknowledged	<i>SM</i> Examiner's Signature Initials				

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## TITLE

Apparatus and method for designing semiconductor integrated circuit

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